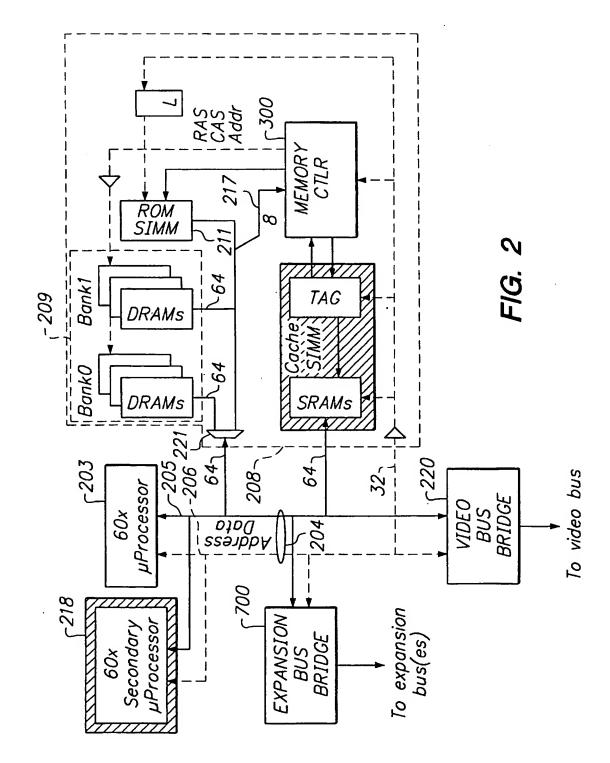
Express Mail No. EL599912565US
Application No. REI of 5,996,036
Title: Bus Transaction Reordering in a Computer System Having Unordered Slaves
Inventors: James D. Kelly *et al.*Atty: Docket No.: 18602-06222
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RE38428

ARBITRATION | SINGLE—BEAT TRANSFER | TERMINATION DATA TENURE **TERMINA TION** F/G. INDEPENDENT ADDRESS AND DATA - ADDRESS TENURE **TRANSFER** ARBI TRA TION

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Inventors: James D. Kelly *et al.*Atty. Docket No.: 18602-06222
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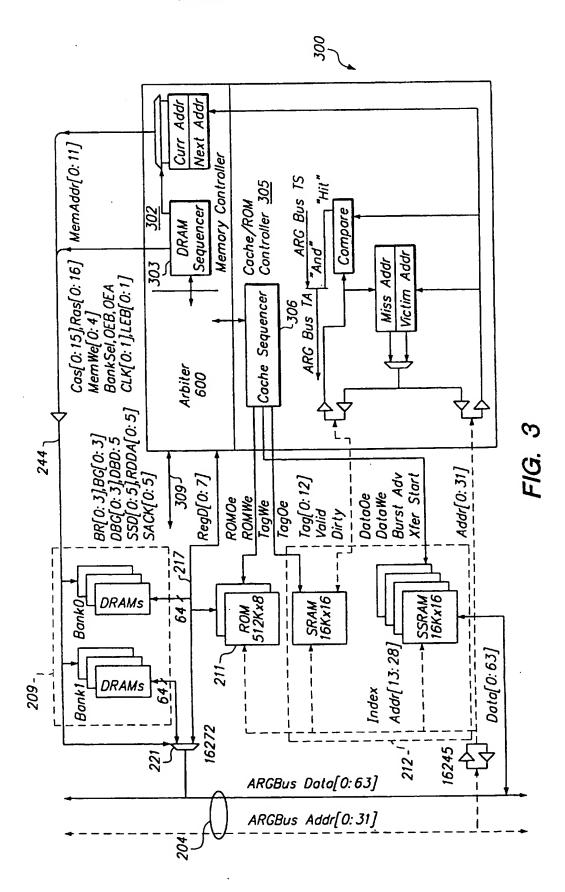
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Application No. REI of 5,996,036
Title: Bus Transaction Reordering in a
Computer System Having Unordered

SLAVES
Inventors: James D. Kelly *et al.*Atty. Docket No.: 18602-06222

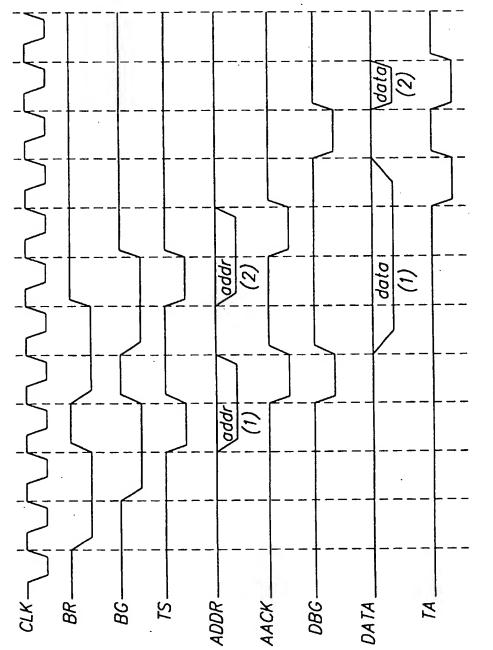
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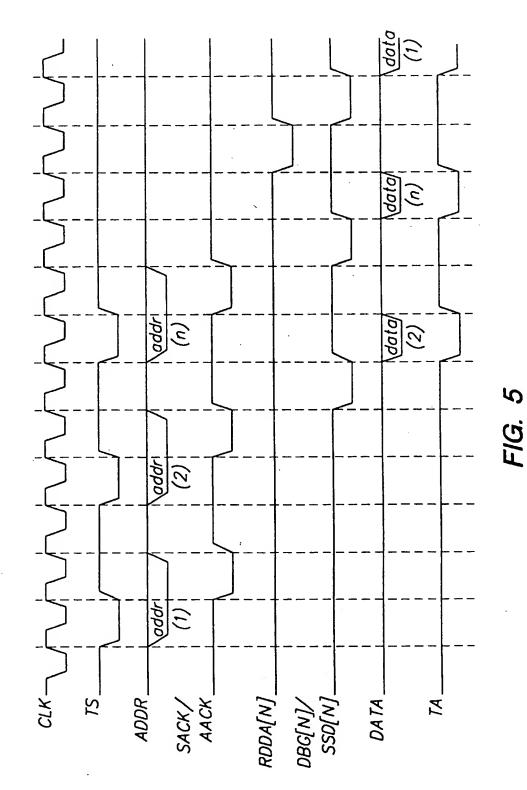
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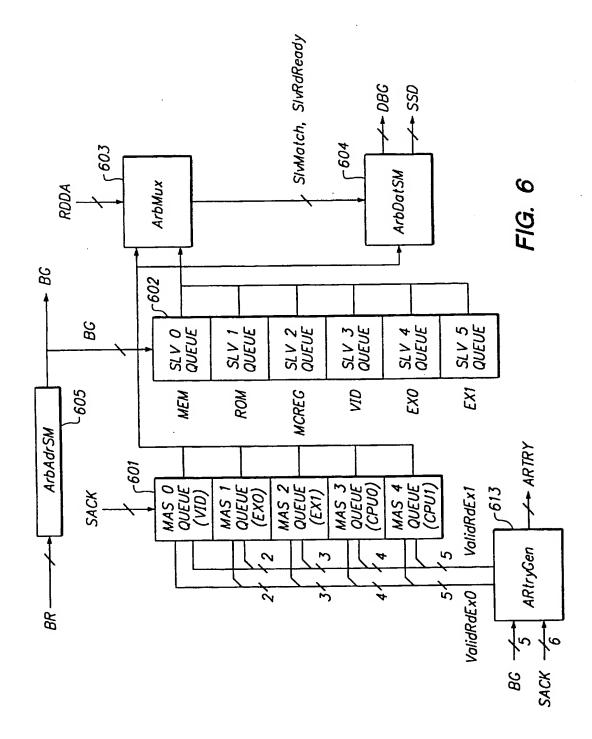
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Express Mail No. EL599912565US Application No. REI of 5,996,036 Title: Bus Transaction Reordering in a

COMPUTER SYSTEM HAVING UNORDERED

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Inventors: James D. Kelly *et al.*Atty. Docket No.: 18602-06222

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F1G. FIG.

PCI Ctrl=FRAME_, IRDY_, TRDY_, STOP_, DEVSEL_, LOCK__ PCI Arb=REQ_, GNI PCIAD[31:0] CBE[3:0] PCI Bus Control [PCIAddrSIv] [PCISlave] [pcism] [PCIMast] Translate Cycle Execute Cycle Translate Cycle Buffer Manager Synchronization Boundary [mgr0]Execute Cycle ARBus Control [AMS1601] [DMst601] [ASIVGO1] [DSINGO1] [ack0]ARBus Ctrl=TS_,AACK_, ARTRY_,TA_,TEA_,TT3, TT1,TBST SACK_, RDDA_, SSD_, DBG_ ARBus Arb=BR_,BG_, ADDR[31:0] rs1z[2:0]

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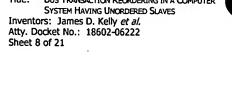
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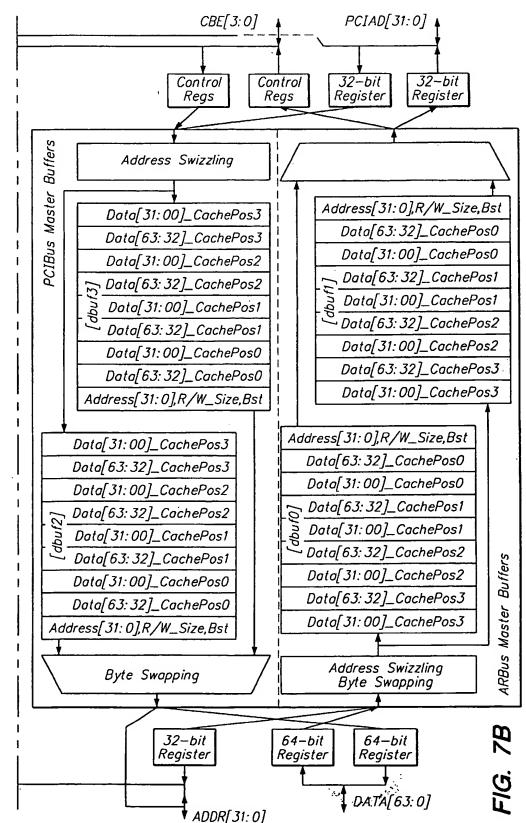
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BUS TRANSACTION REORDERING IN A COMPUTER





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Computer System Having Unordered

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Rd Mem
III

Rd µP1

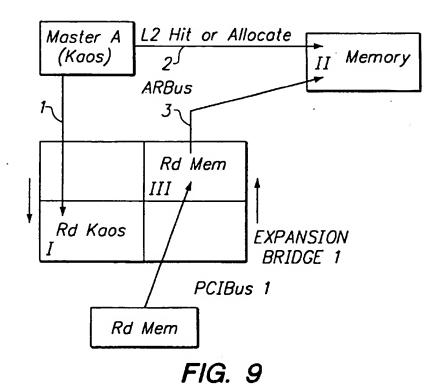
Rd µP1

RD EXPANSION
BRIDGE 1

PCIBus 1

FIG. 8

Rd Mem



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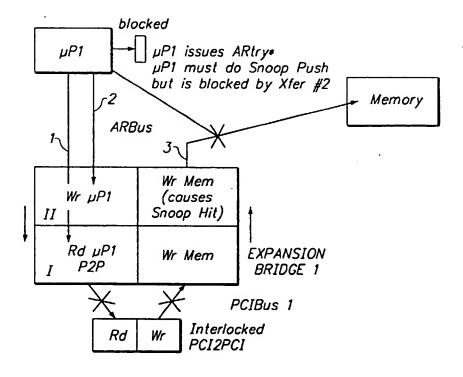


FIG. 10

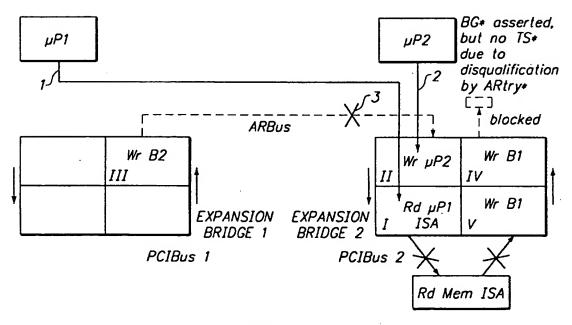
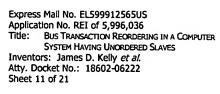


FIG. 11

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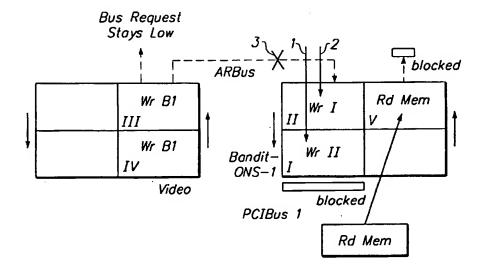


FIG. 12

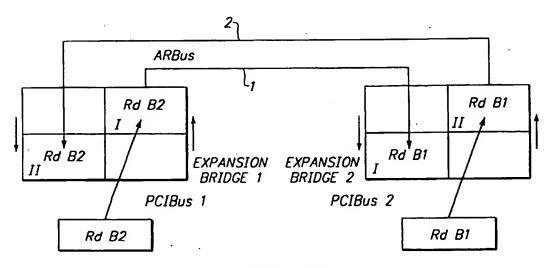


FIG. 13

Express Mail No. EL599912565US Application No. REI of 5,996,036 Title: Bus Transaction Reorder BUS TRANSACTION REORDERING IN A

COMPUTER SYSTEM HAVING UNORDERED SLAVES

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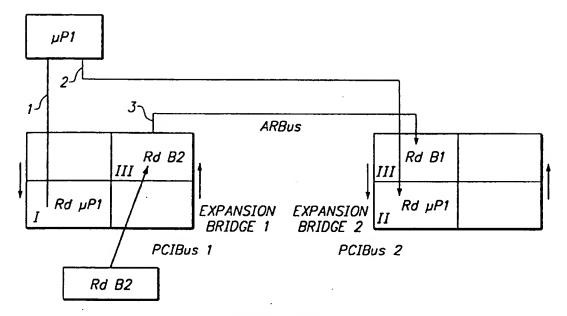


FIG. 14

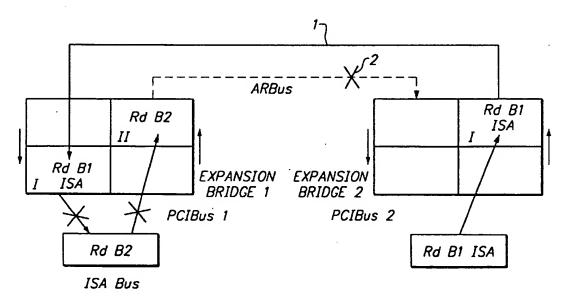


FIG. 15

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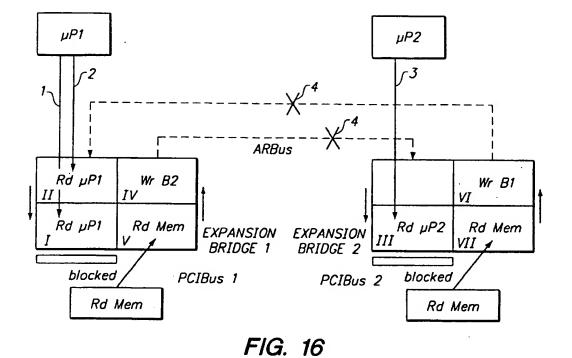
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μP1 μP2 r2 **ARBus** Wr B2 Rd B2 Rd B1 IIIRd µP1 Rd Mem Rd µP2 ISA ISA **EXPANSION EXPANSION** BRIDGE 1 BRIDGE 2 PCIBus 1 blocked PCIBus 2 Rd Mem ISA Rd B1 ISA Bus

FIG. 17

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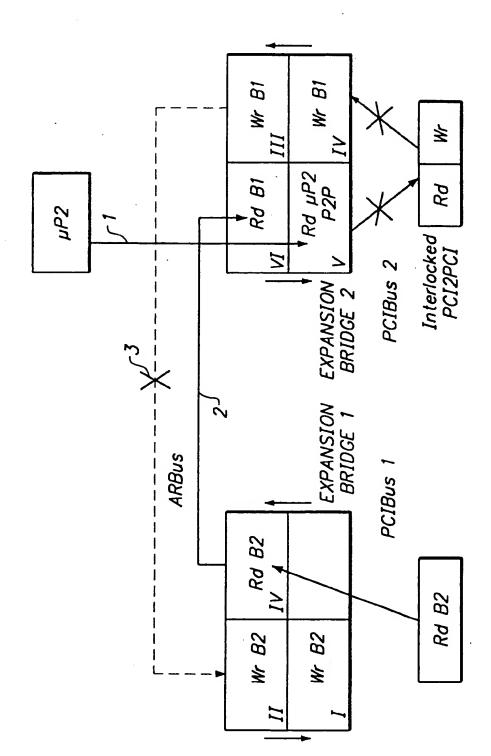
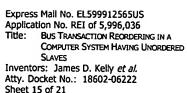
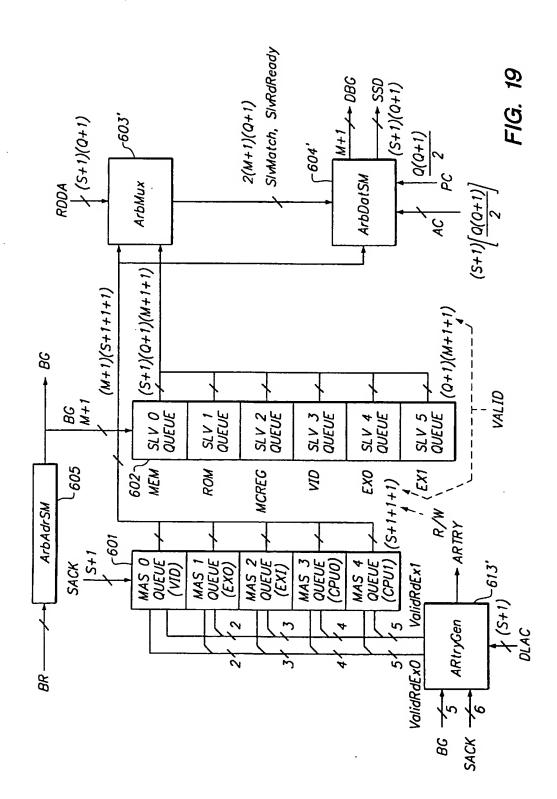


FIG. 18

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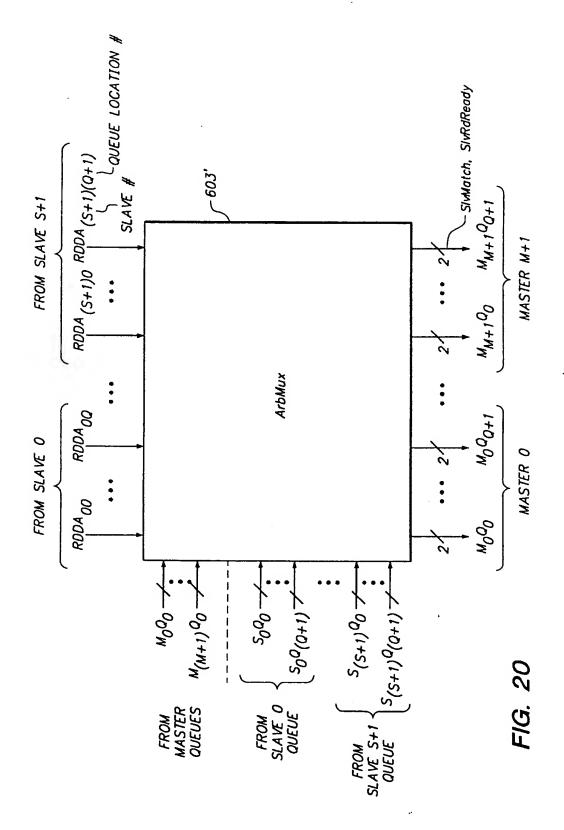
Application No. REI of 5,996,036

Title: Bus Transaction Reordering in a Computer System Having Unordered Slaves

Inventors: James D. Kelly et al.

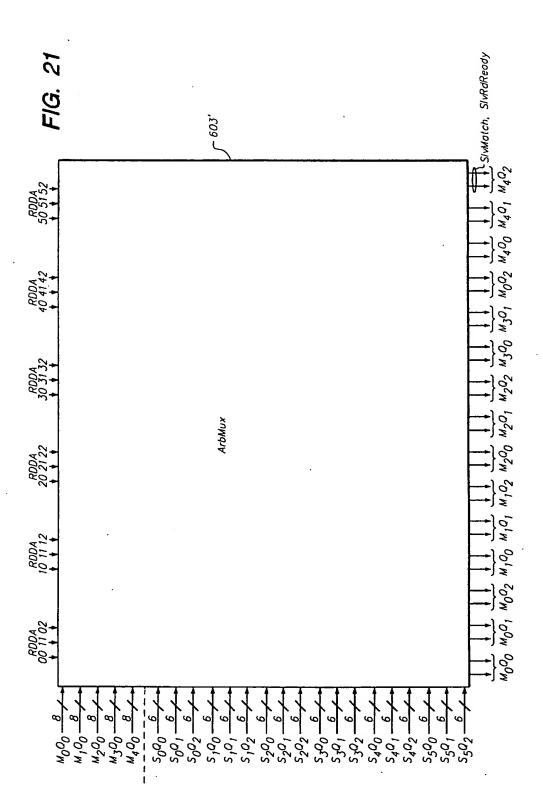
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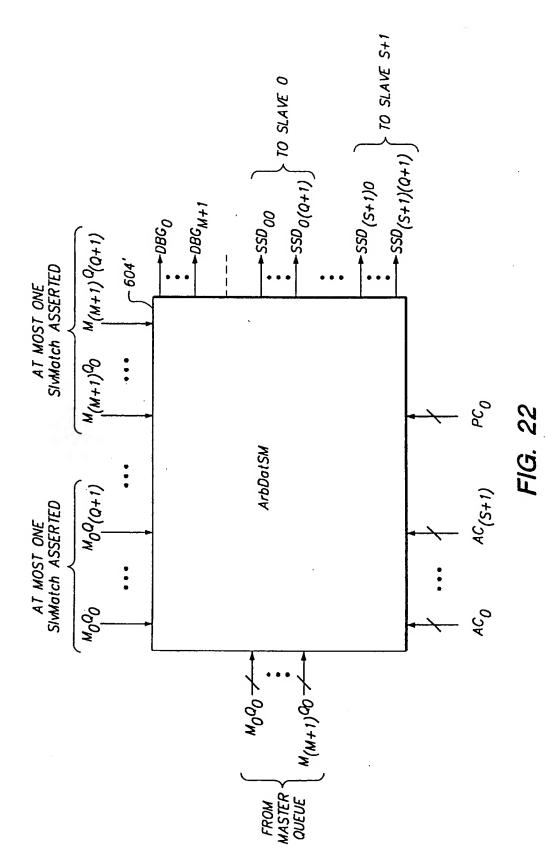
Application No. REI of 5,996,036

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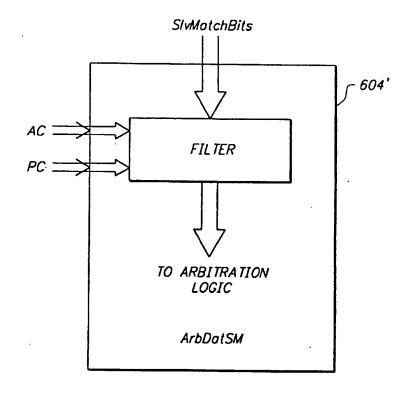
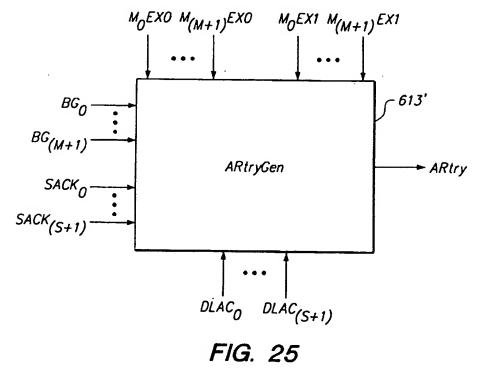
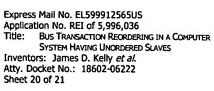
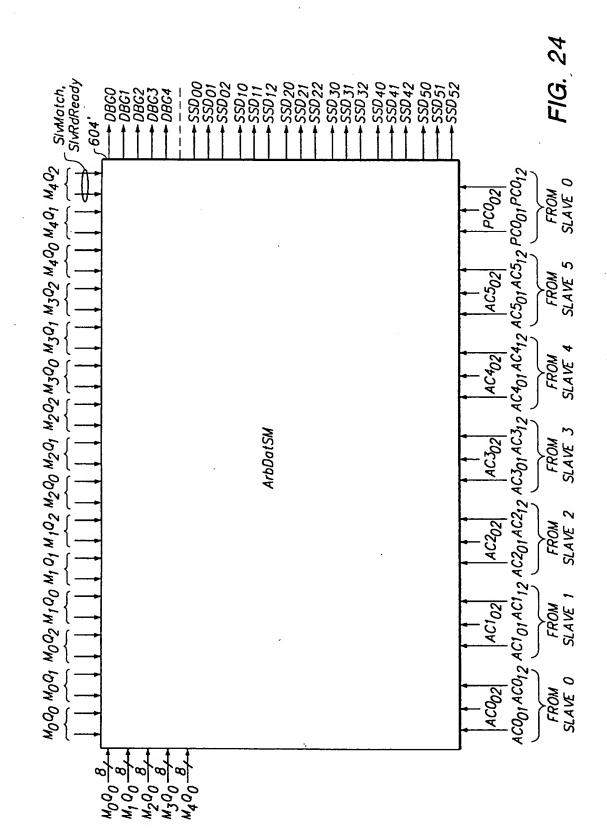


FIG. 23







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